

REMARKS

Claims 1, 3-6, 9, and 11 stand rejected under 35 U.S.C. §102(b) as being anticipated by Klim. In response, Applicants have amended independent claims 1 and 9 to define a logic circuit in the preamble of each claim, and to positively define a pseudo-NMOS circuit in the body of each claim, respectively. As applied to the claims as amended, Applicants respectfully traverse the rejection, as Klim fails to teach or suggest at least a pseudo-NMOS circuit. Instead, Klim is directed to a domino logic circuit. By definition, a pseudo-NMOS circuit has a pull-up load device that is always ON. However, Klim teaches that channel transistor 27c is instead controlled by a clock signal. Please see FIG. 2 of Klim, for example.

As a pseudo-NMOS circuit is now positively recited in claims 1 and 9, this feature should be given patentable weight. Accordingly, Applicants respectfully submit that claims 1 and 9, and dependent claims 3-8 and 11, respectively, are allowable over the references of record, including Klim.

The Examiner has indicated that claims 12-14 are allowed. Applicants acknowledge and appreciate the Examiner's statement. The Examiner also has stated that claims 7 and 8 would be allowable if rewritten in independent form. At this time, Applicants elect to keep the claims in their present form pending the Examiner's reply to the response herein.

For at least the foregoing reasons, Applicants believe that this case is in condition for allowance, which is respectfully requested. The Examiner should call Applicants' attorney if an interview would expedite prosecution.

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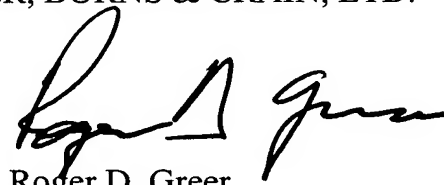
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Respectfully submitted,

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